Announcing IBM System/370 Model 135 The model 135 is a new System/370 designed for both commercial and scientific use.

The internal processing speed is similar to that of the System/360 model 50, depending on the instruction mix.

The System/370 model 135 is a growth system, and is an attractive entry point for current System/360 model 25 and 30 users.

# Compatibility:

The model 135 is program compatible with System/360 programs (Excepting time or model dependent programs) and System/360 model 20).
Emulation for 1401, 1440, 1460 is available as a feature.

# Technology:

- Monolithic System Technology (MST-2) is used for logic circuitry.
- Phase 2 I Monolithic circuits are used for all 135 storage
- A Motor Generator set supplies power to the 135.

### Storage:

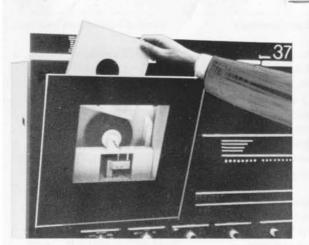
- The model 135 has non destructive read-out storage.
- Main Storage sizes are available from 96K up to 240K bytes.
- A read cycle will access 72 bits of main store- 64 data bits and 8 Error Correction Code bits.
- Control store can be expanded from 24k to 48k according to the feature configuration.
- Control store is loaded from a flexible disk in the console file.



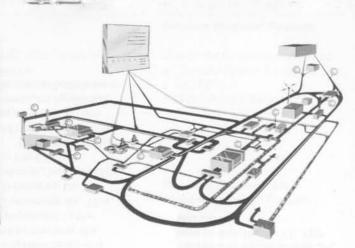
### Maintenance Features:

The 135 has CE Maintenance Facilities normally only found in larger systems:

- Error checking and indicators-including the display roller blind.
- Fault locating microdiagnostics which are loaded from the Console File to check CPU logic before the microprogram is loaded. The microdiagnostics are automatically loaded with the power on sequence.
- Keyboard Alter/Display functions controlled by the microprogram.
- Automatic System Checkout Program (ASCP) to exercise the I/O devices. Loaded via the Console File.
- Impulse Check Routines are loaded from the console file and used to check the 3210 and 2319.
- Error Correction Code (ECC)-The 135 detects and corrects single bit errors. Double, and some multiple bit errors are detected but not corrected.
   ECC reduces the effect of most intermittent memory failures and allows maintenance to be deferred on solid single bit errors.
- Channel Command Retry-All information required to retry a channel operation is made available to the operating system.
- Instruction Retry—When a CPU error is detected during the execution of any instruction, the processor will automatically attempt to retry the operation—This will reduce the effect of intermittents.
- Logout-Error data from machine and channel checks is logged out on the systems residence device.







CONSOLE FILE

CONSOLE

DATA FLOW

# Systems Diagnostic Support:

- OLT's On Line Test—is used to diagnose and verify repairs on I/O.
- OLTEP On Line Test Executive Program—is available as part of the type 1 Operating
  System and allows the OLT and customer operation to run concurrently. It is run as a problem program to select, load and execute the OLT.
- OLTSEP On Line Test
   Standalone Executive Program
   —is a standalone version of
   OLTEP and is designed as a
   control program to select,
   load and execute OLTs for
   checking the System I/O at
   installation or when the Operating System is not available.
- error EREP Environment Recording Edited Print—is part of the Type 1 Operating System. It will run as a problem program to record the system environments, format them and print the edited results.
- SEREP Standalone Environment Recording Edited Print—is a program used to capture edit and print environment data in those cases where the system failed and was unable to transmit the failing environment to the SYS LOG recording set. This program is designed for use by the customer, but may also be used by the CE.
- ASCP Automatic System
   Checkout Program—is used to
   insure that when all System
   Commponents are hooked to gether, the whole System functions correctly.

 SOSP Standalone On Line Test Support Processor operates as a modular program, called and loaded by OLTSEP to generate and maintain a configured and edited Master IPL tape or disk file for OLTEP and OLTS.

#### Standard Hardware Features:

- Byte Multiplexor Channel
- Time of Day Clock
- Control Registers
- Storage and Fetch Protect
- Floating Point Arithmetic Instructions
- Control Storage (24K)
- Main Storage of 96K to 240K bytes
- Decimal Arithmetic Instructions

# **Optional Hardware Features:**

- Block Multiplexing
- Extended Precision Floating Point Arithmetic
- Integrated 2319 Attachment
- Selector Channel 2 + 3
- DOS Compatibility/Emulation
- Direct Control

- 1401/1440/1460 Compatible Emulation
- Integrated Communications Adapter with Start/Stop and BSC Terminals
- Additional 12K or 24 Control Store

# Selective Hardware Features:

One of the following is required:

- Console Printer Keyboard (3210-1)
- 3215 Printer Keyboard

This is a CE Career Path
"General Systems" Product.

IBM World Trade Corporation DP Customer Engineering 821 United Nations Plaza New York, N.Y. 10017, U.S.A.

